

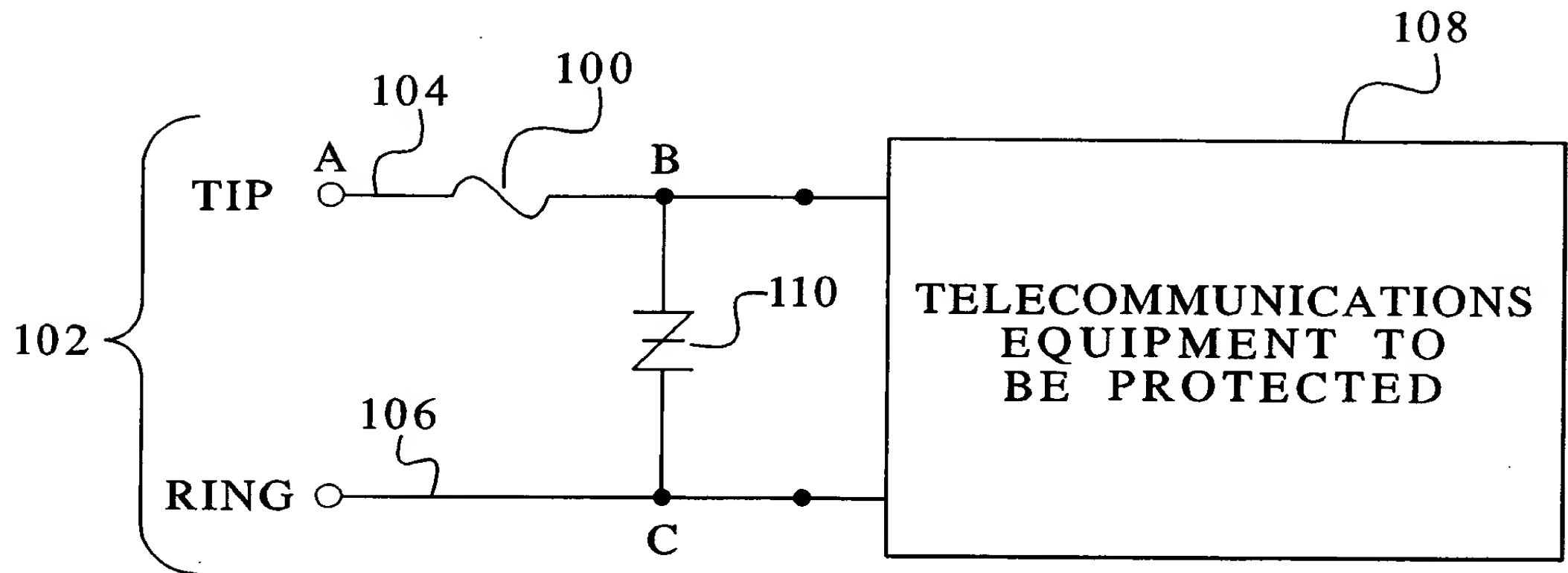


Title: INTEGRATED OVERCURRENT AND OVERVOLTAGE APPARATUS  
FOR USE IN THE PROTECTION OF TELECOMMUNICATION  
CIRCUITS

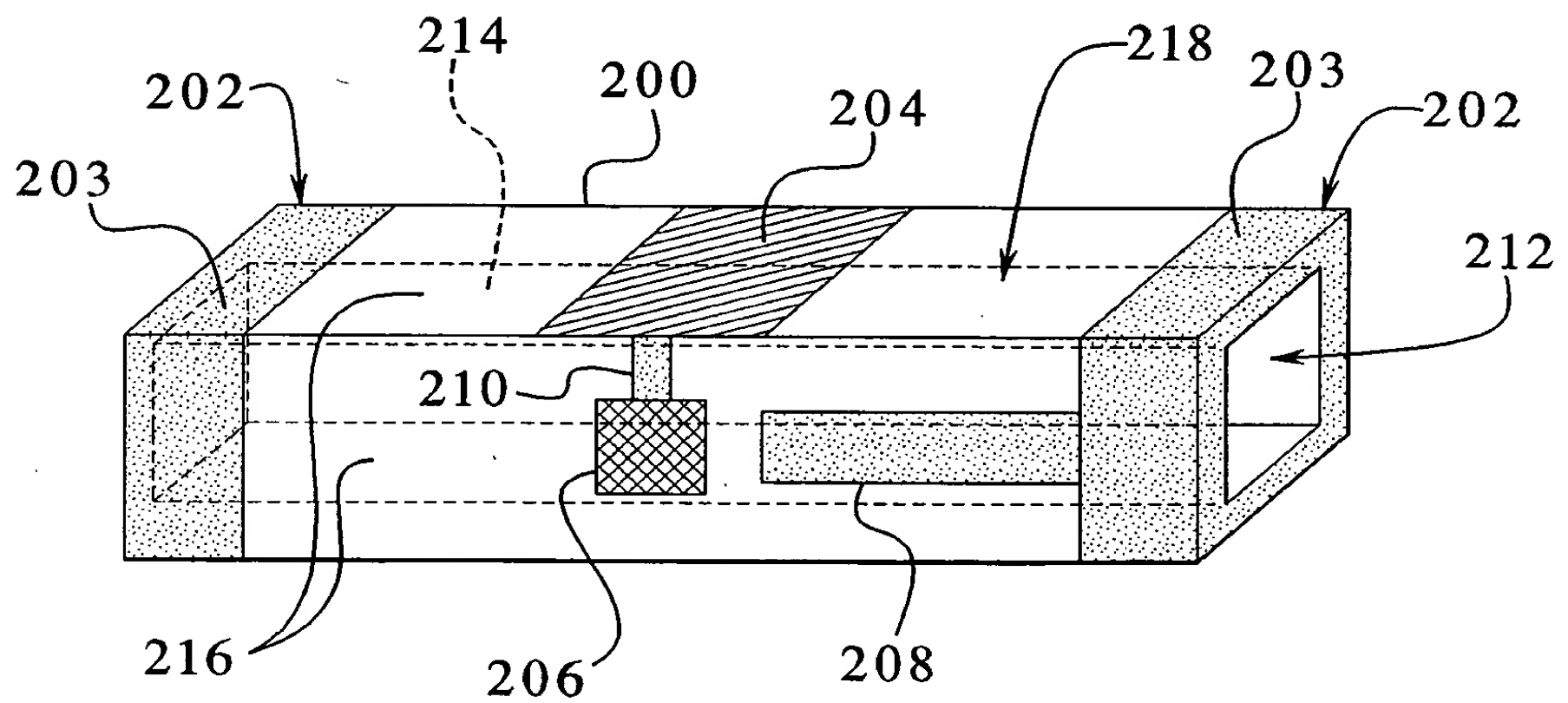
Inventor: Stephen J. Whitney  
App. No.: 09/649,195  
Docket No.: 0112690-004

Replacement Drawing

**FIG.1**  
**(PRIOR ART)**



**FIG.2**



Inventor: Stephen J. Whitney  
App. No.: 09/649,195  
Docket No.: 0112690-004

### Replacement Drawing

FIG. 3

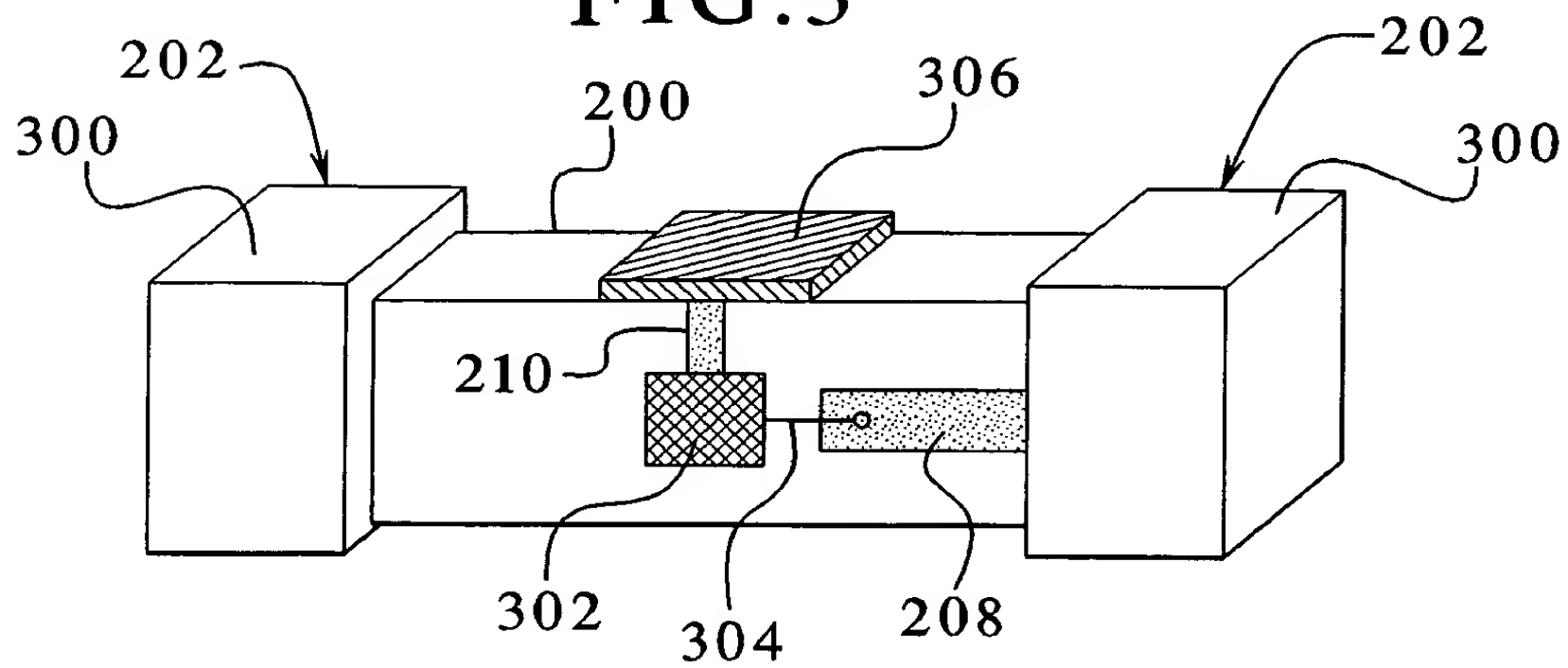
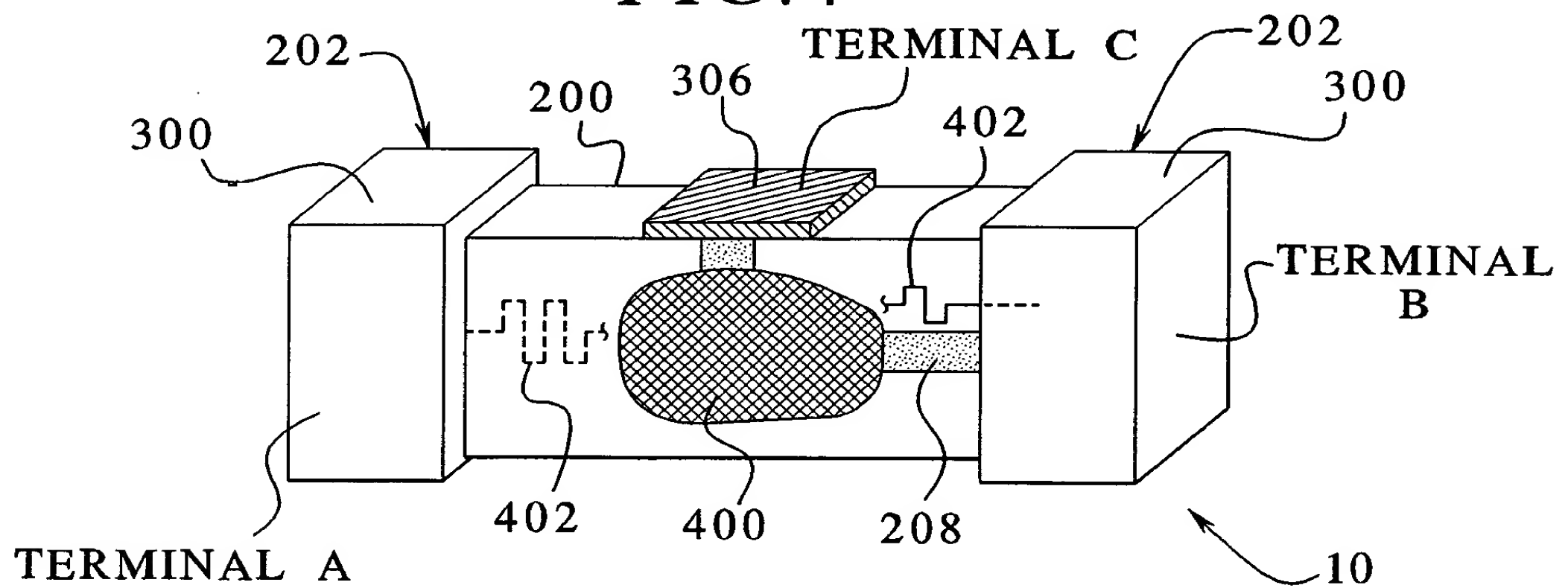
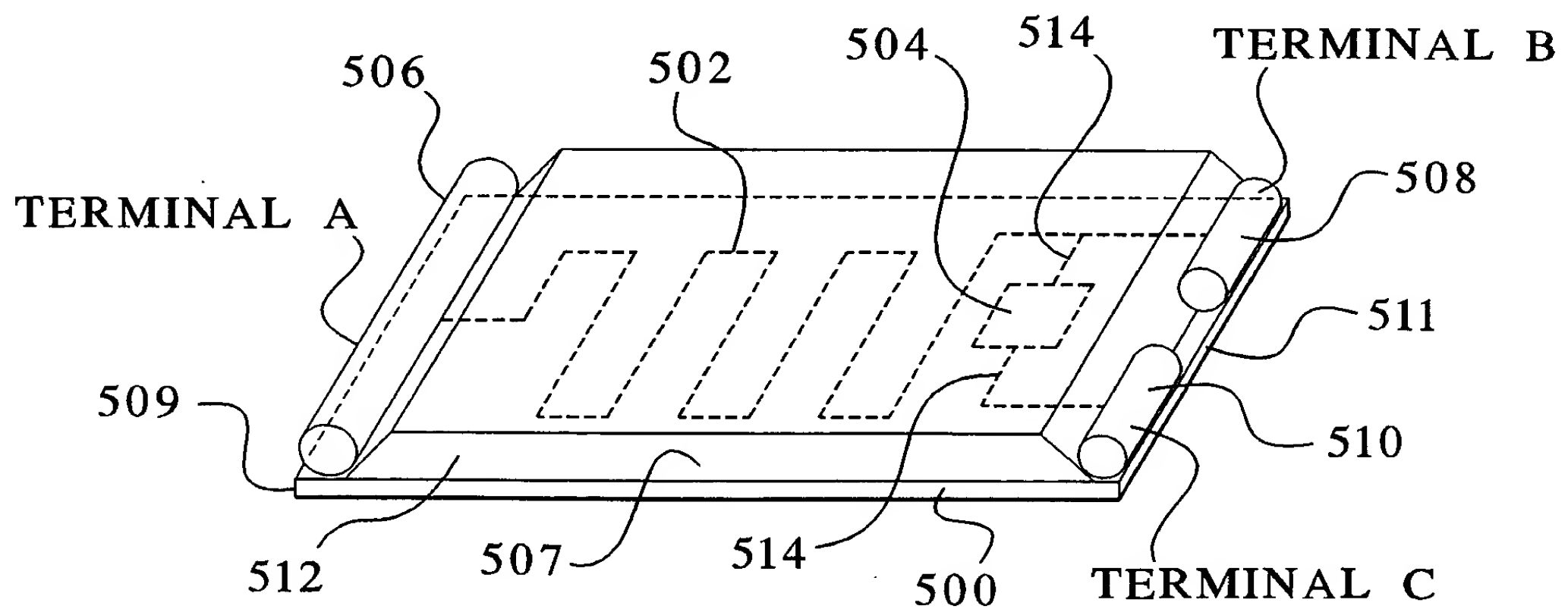


FIG.4



**FIG.5**



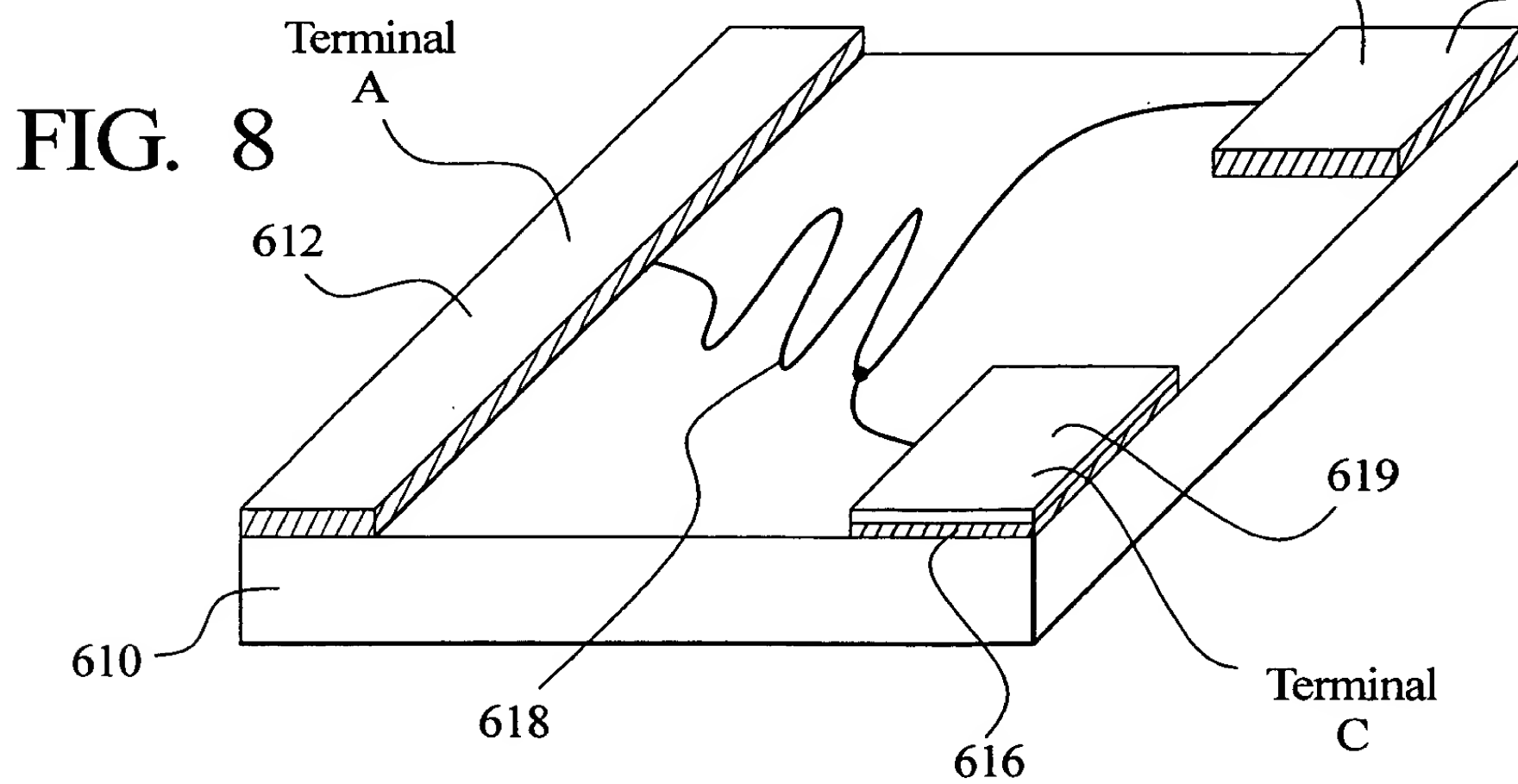
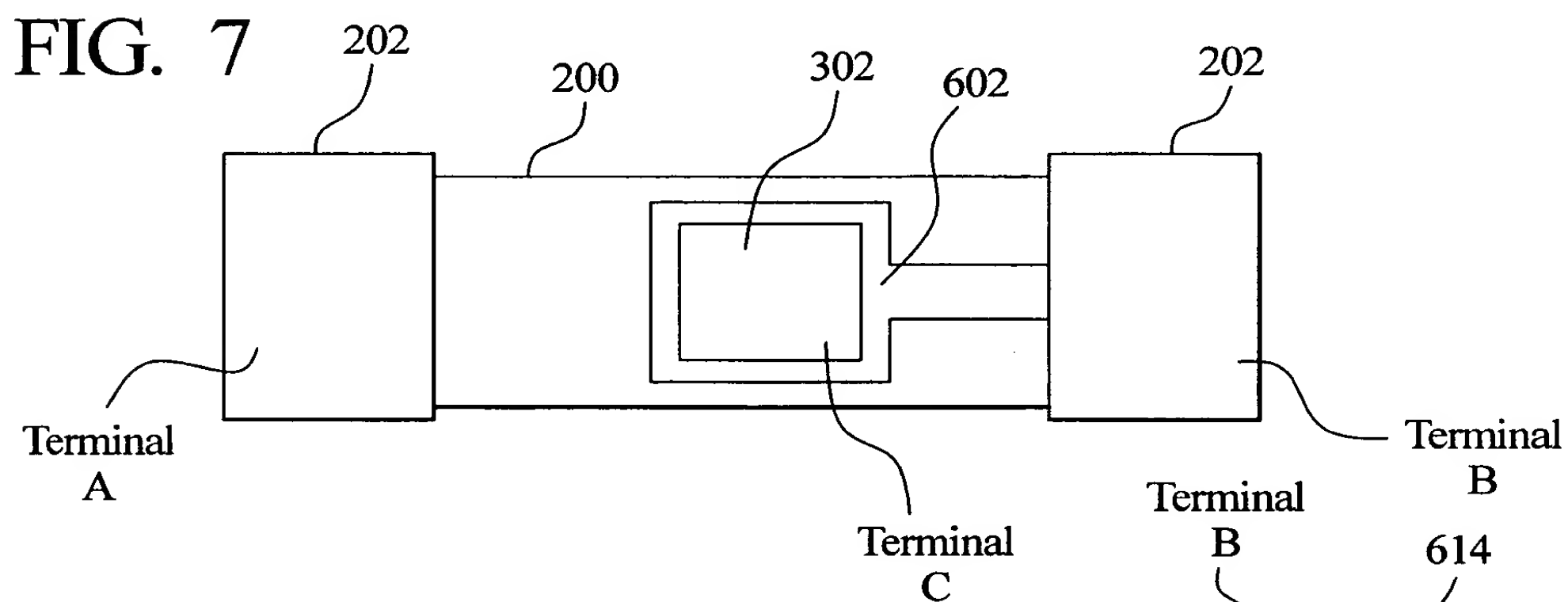
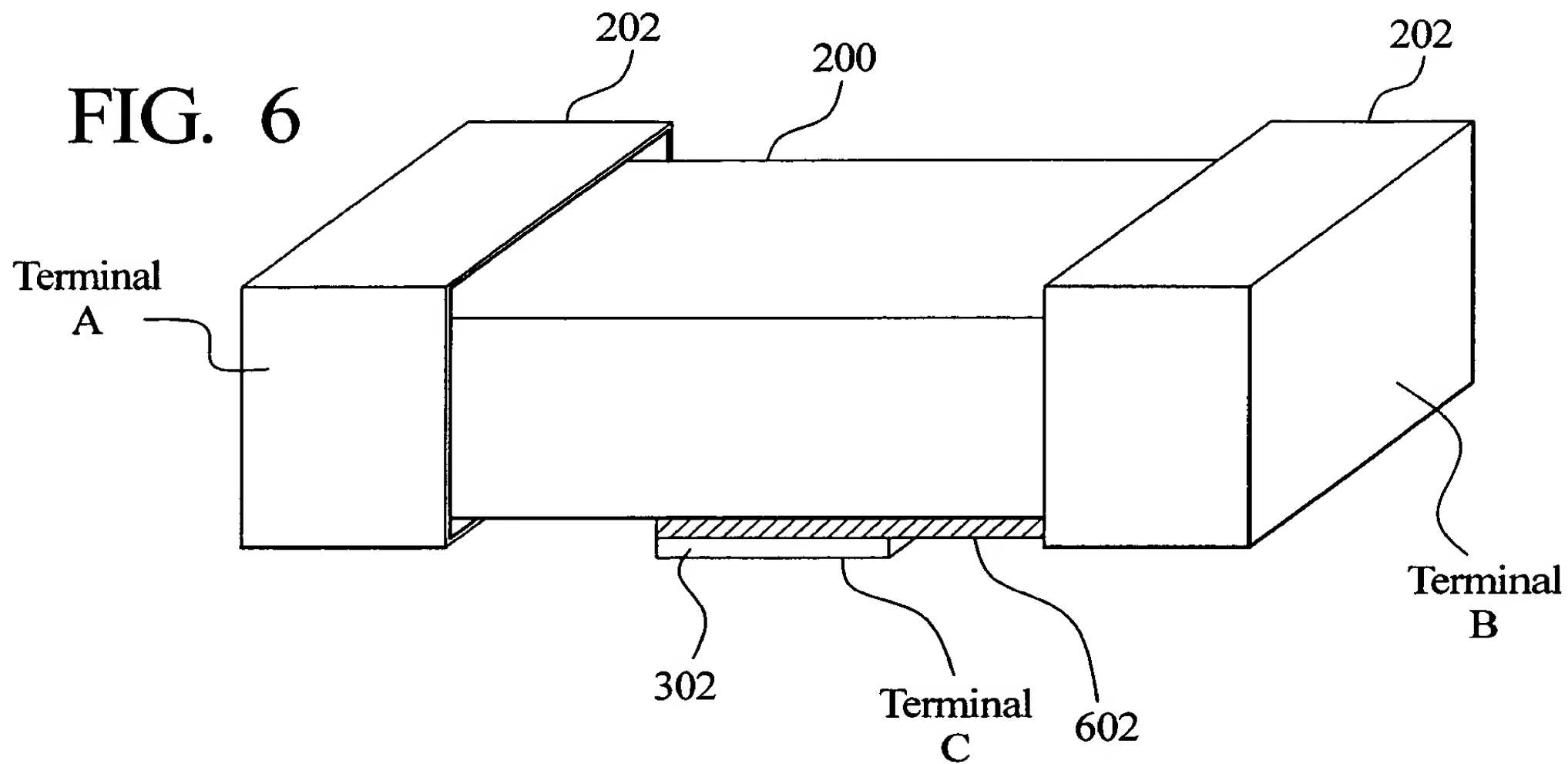


FIG. 9

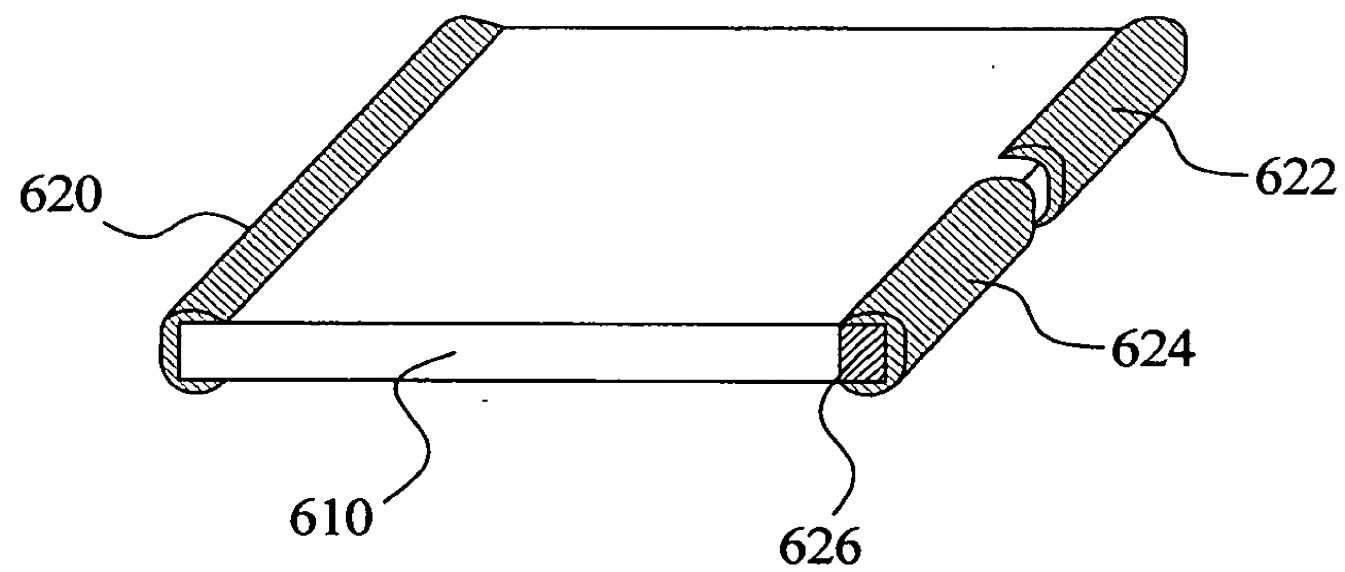


FIG. 10

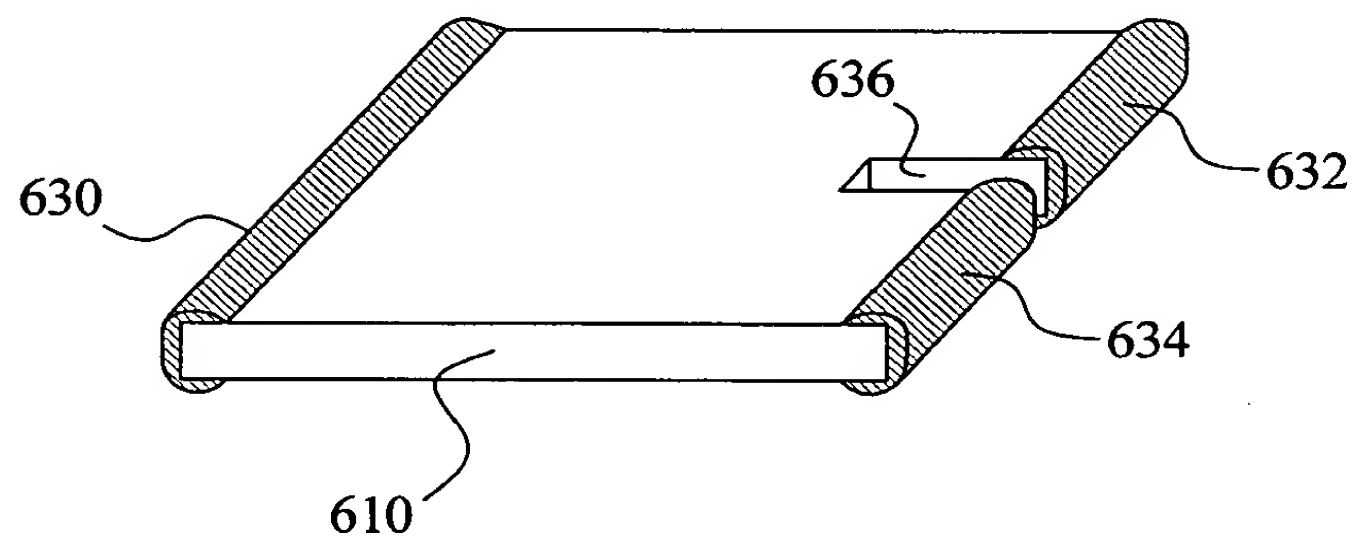


FIG. 11

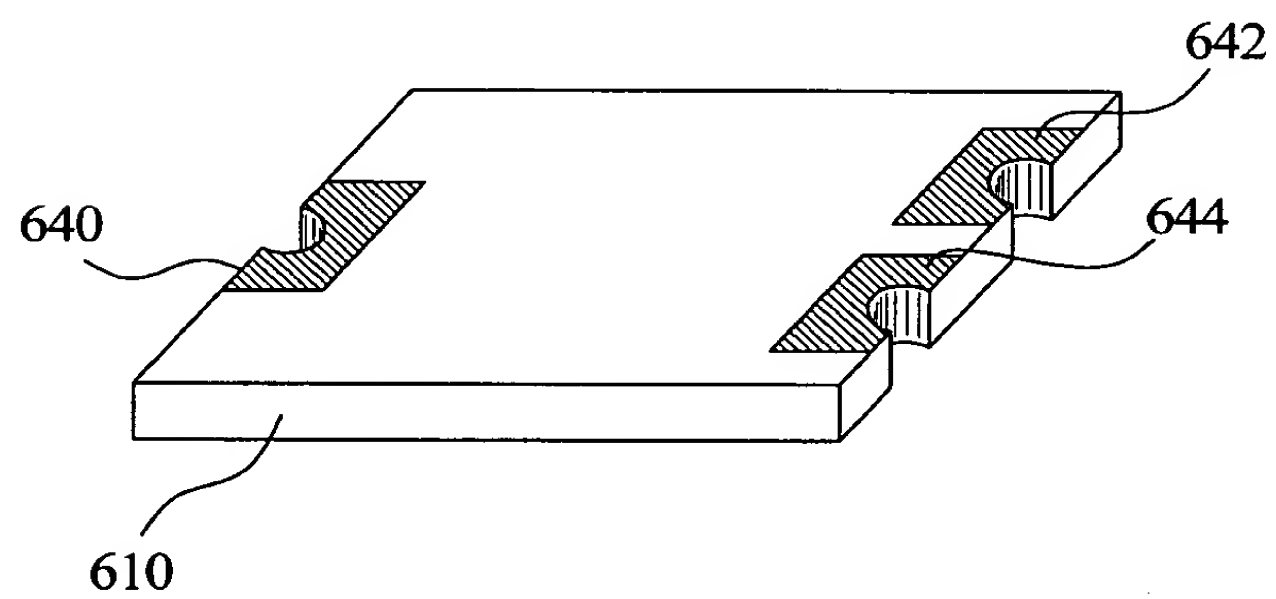


FIG. 12

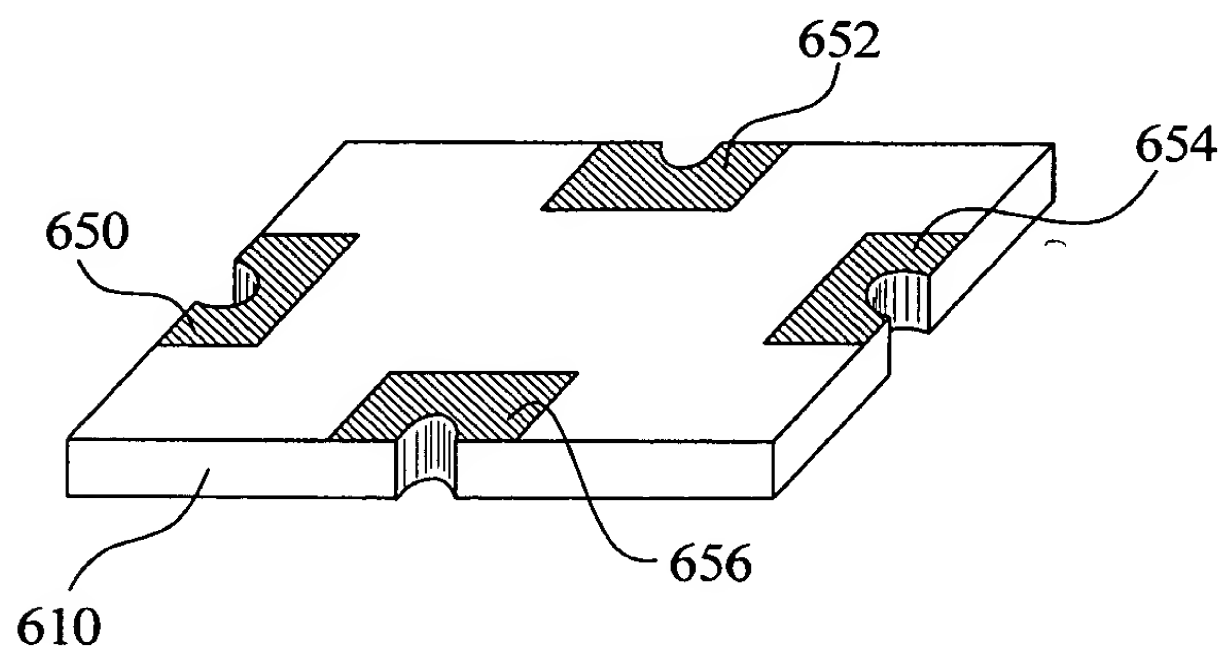


FIG. 13

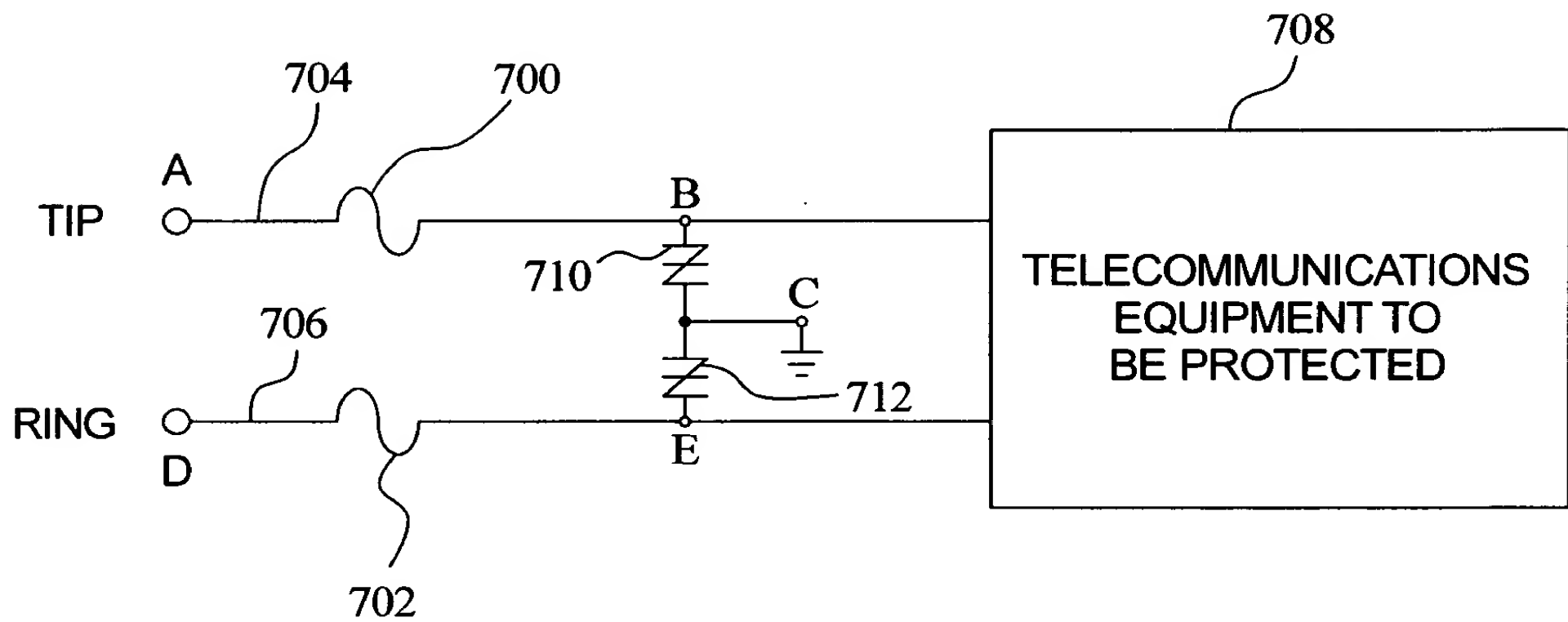


FIG. 14

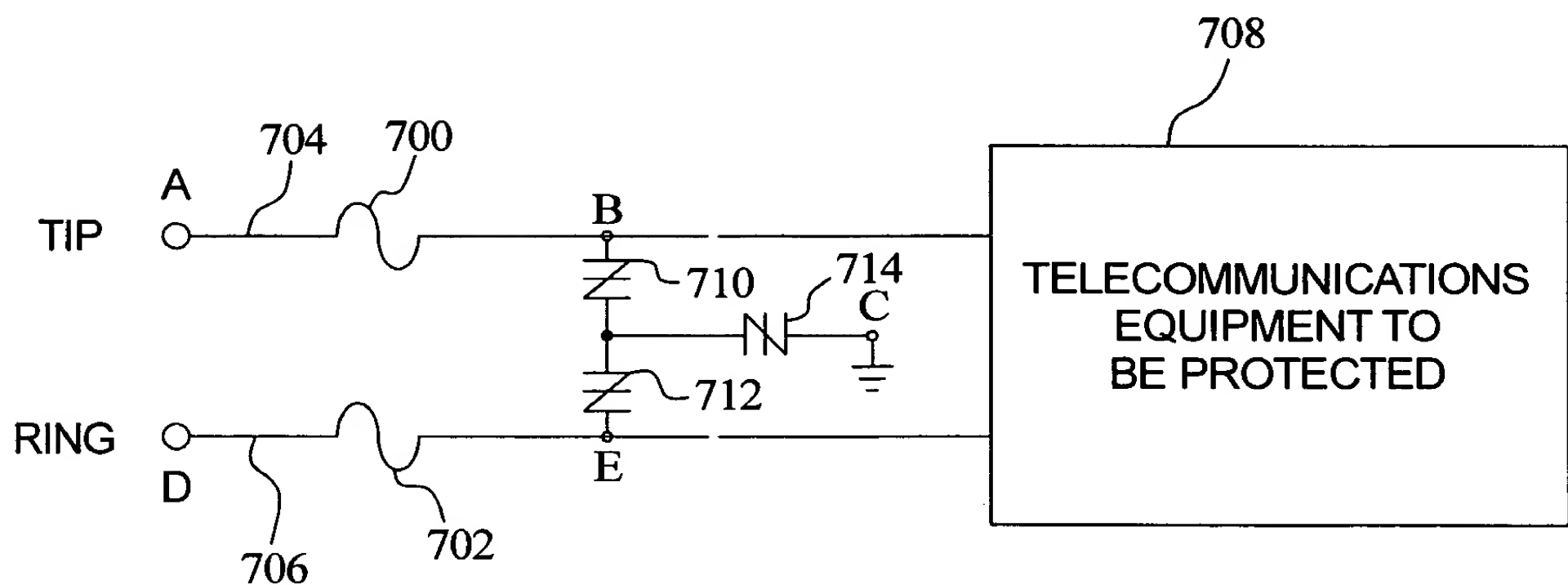


FIG. 15

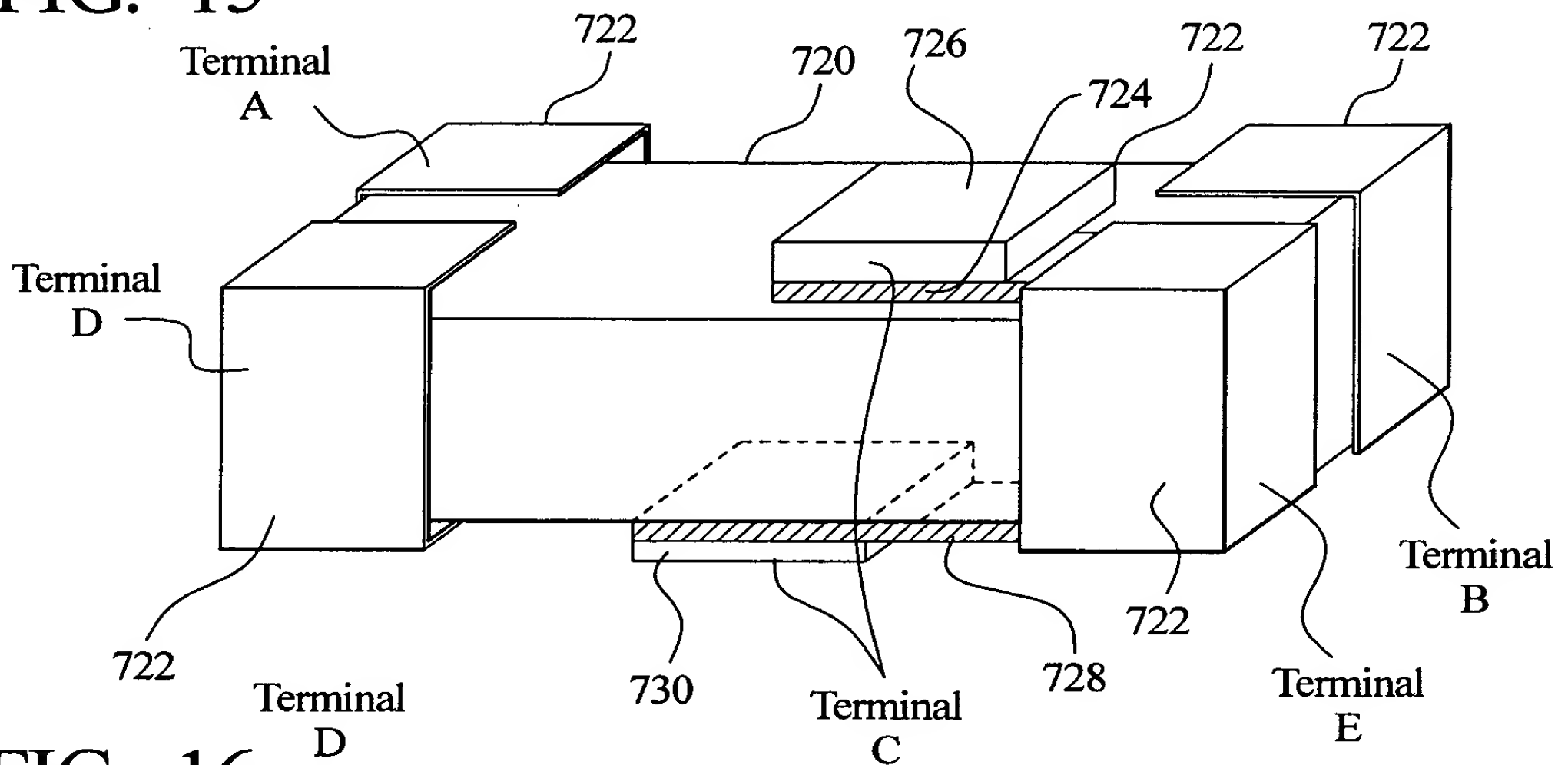


FIG. 16

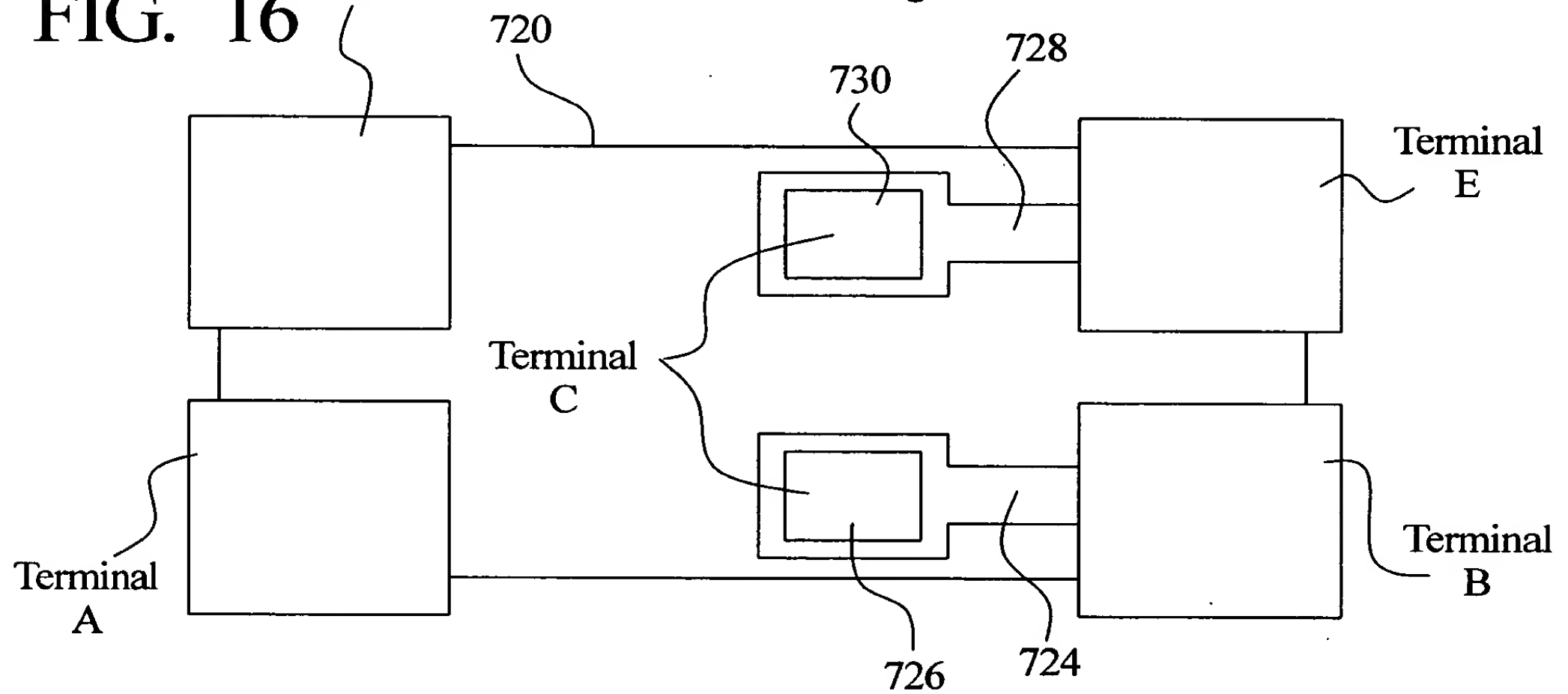


FIG. 17

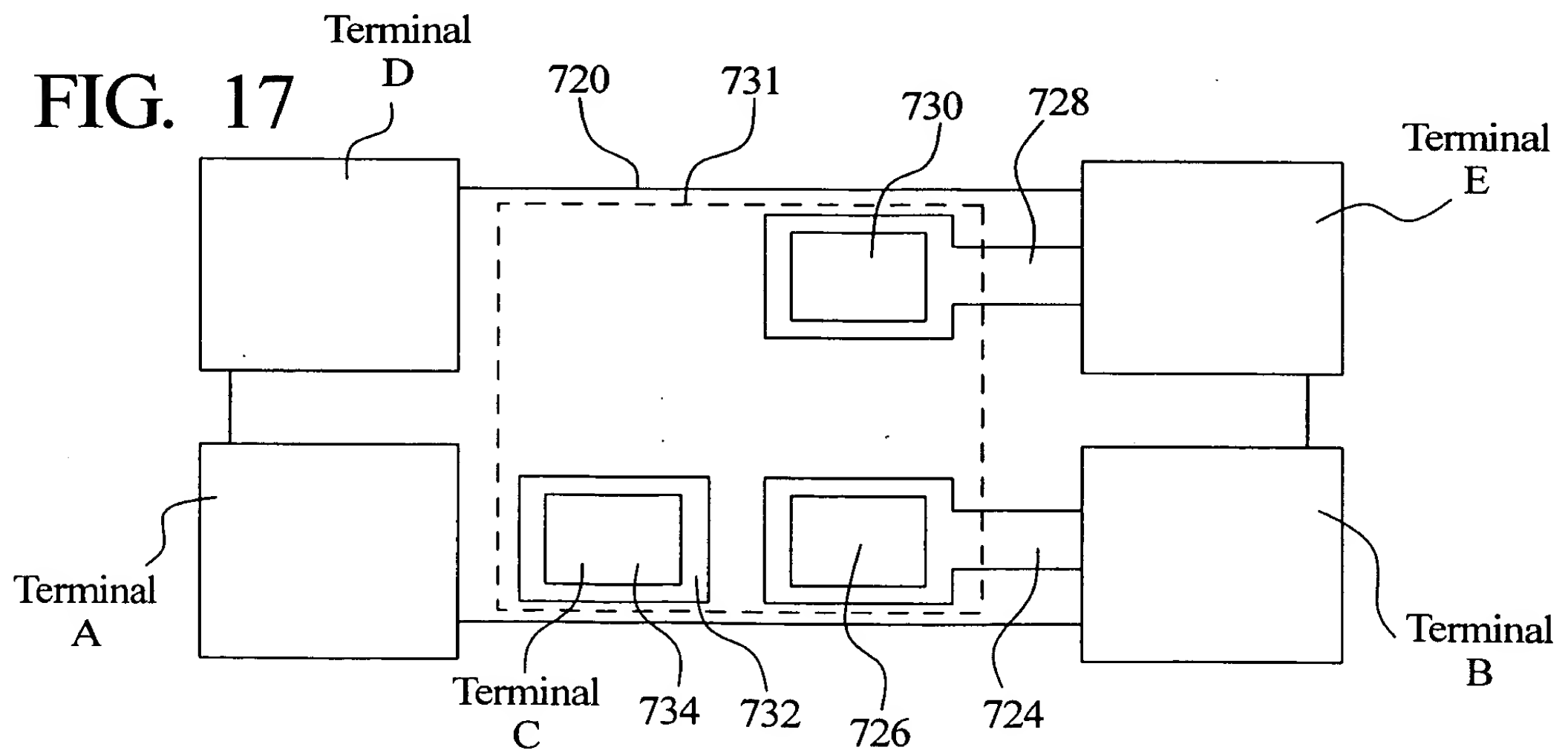


FIG. 18

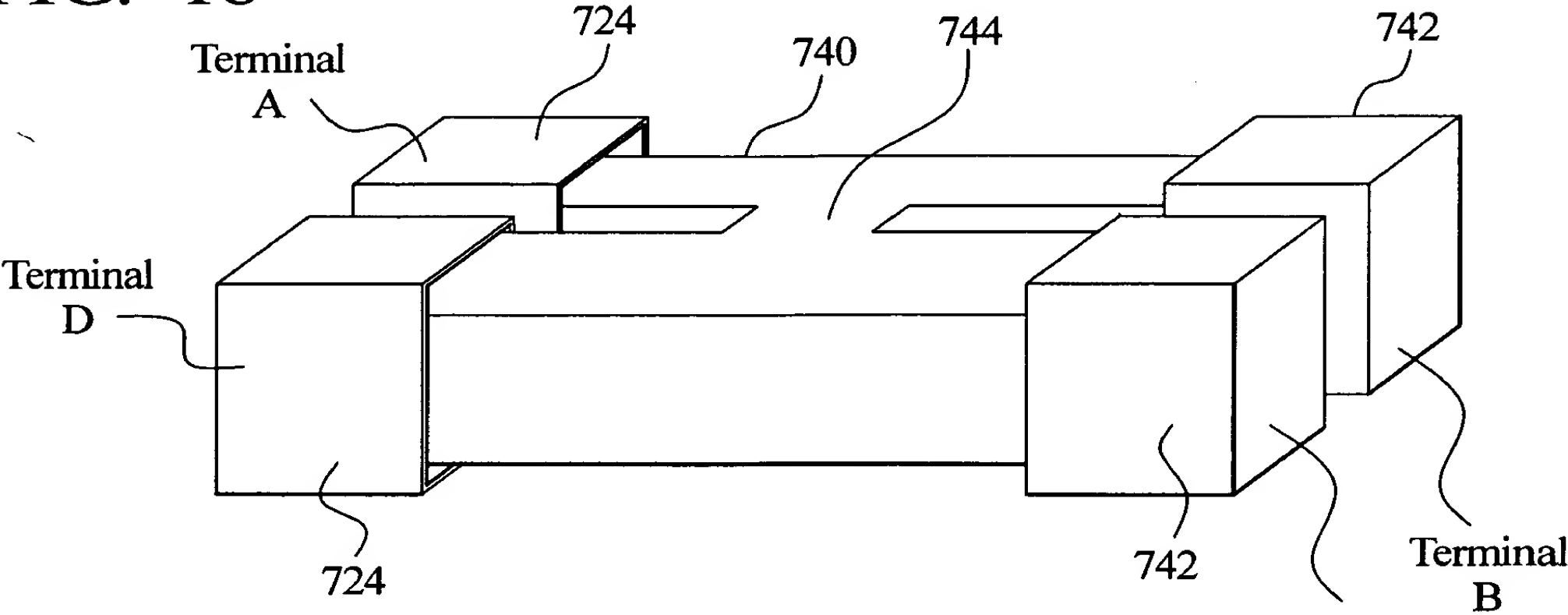


FIG. 19

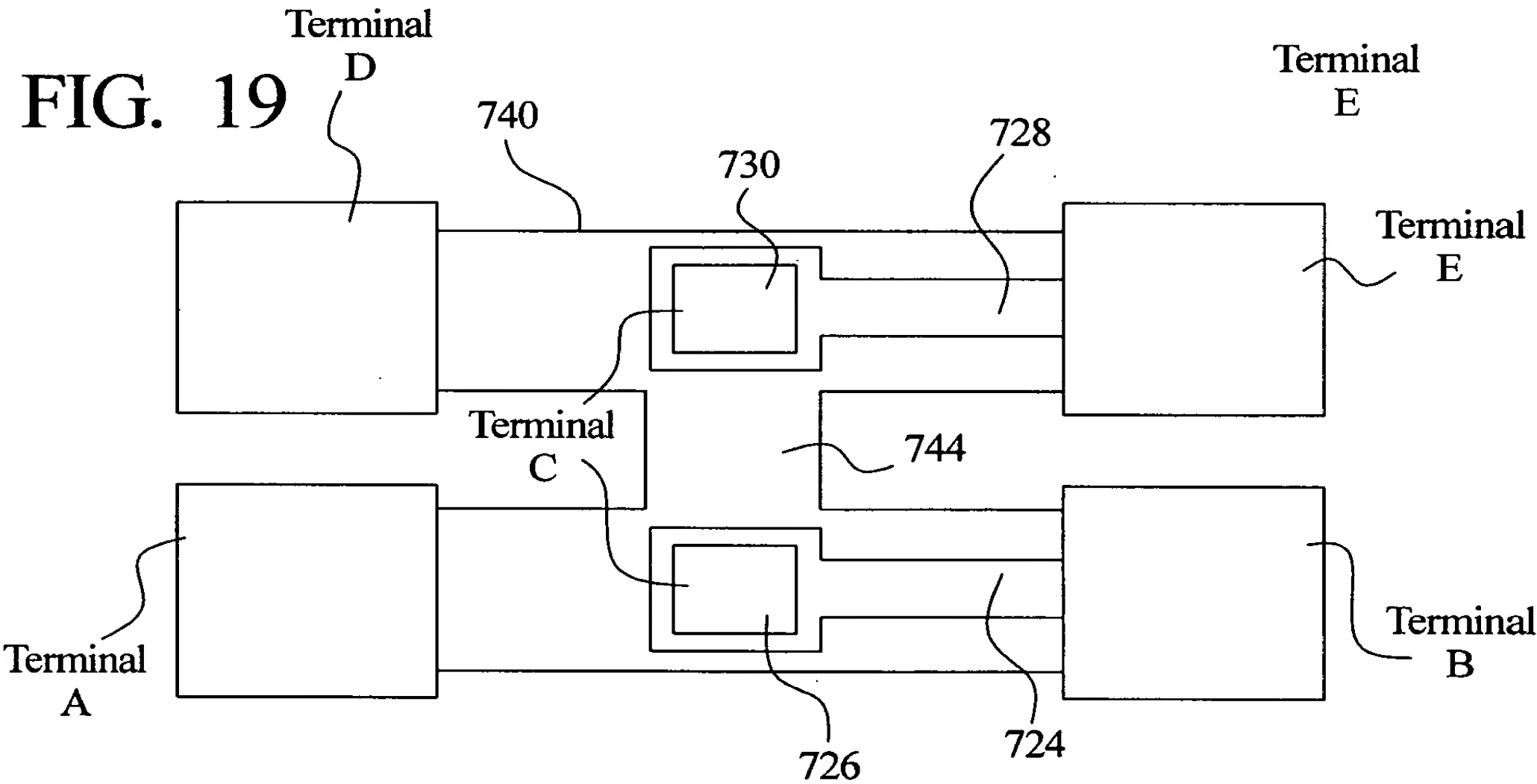


FIG. 20

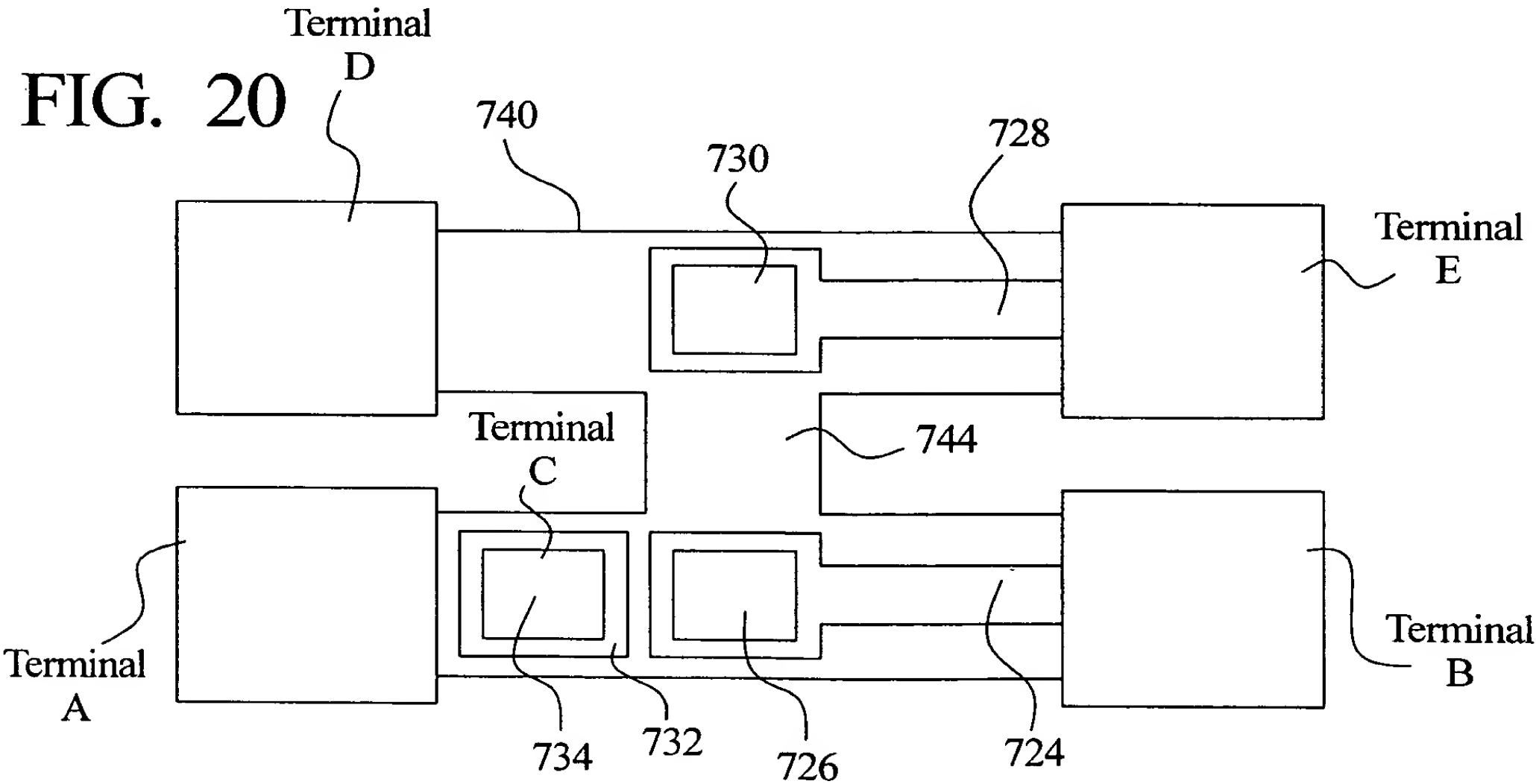


FIG.21

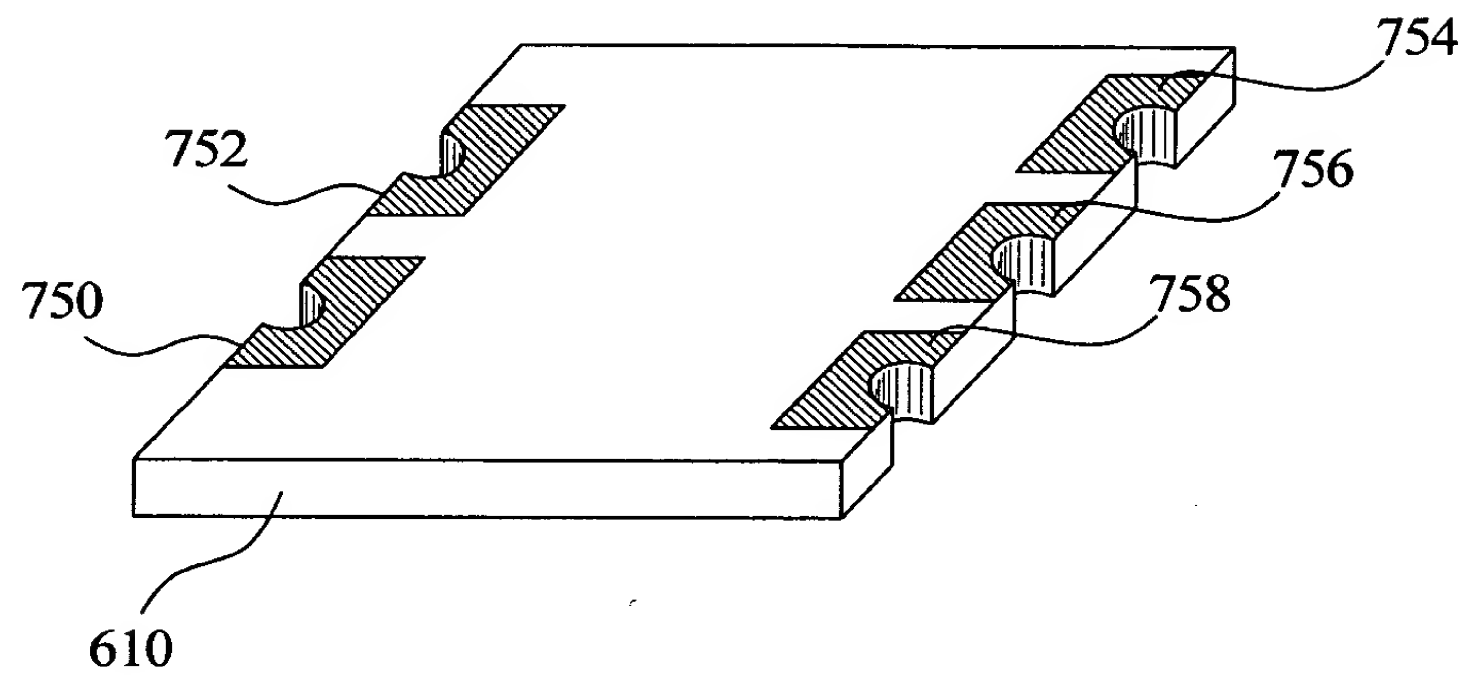


FIG. 22

